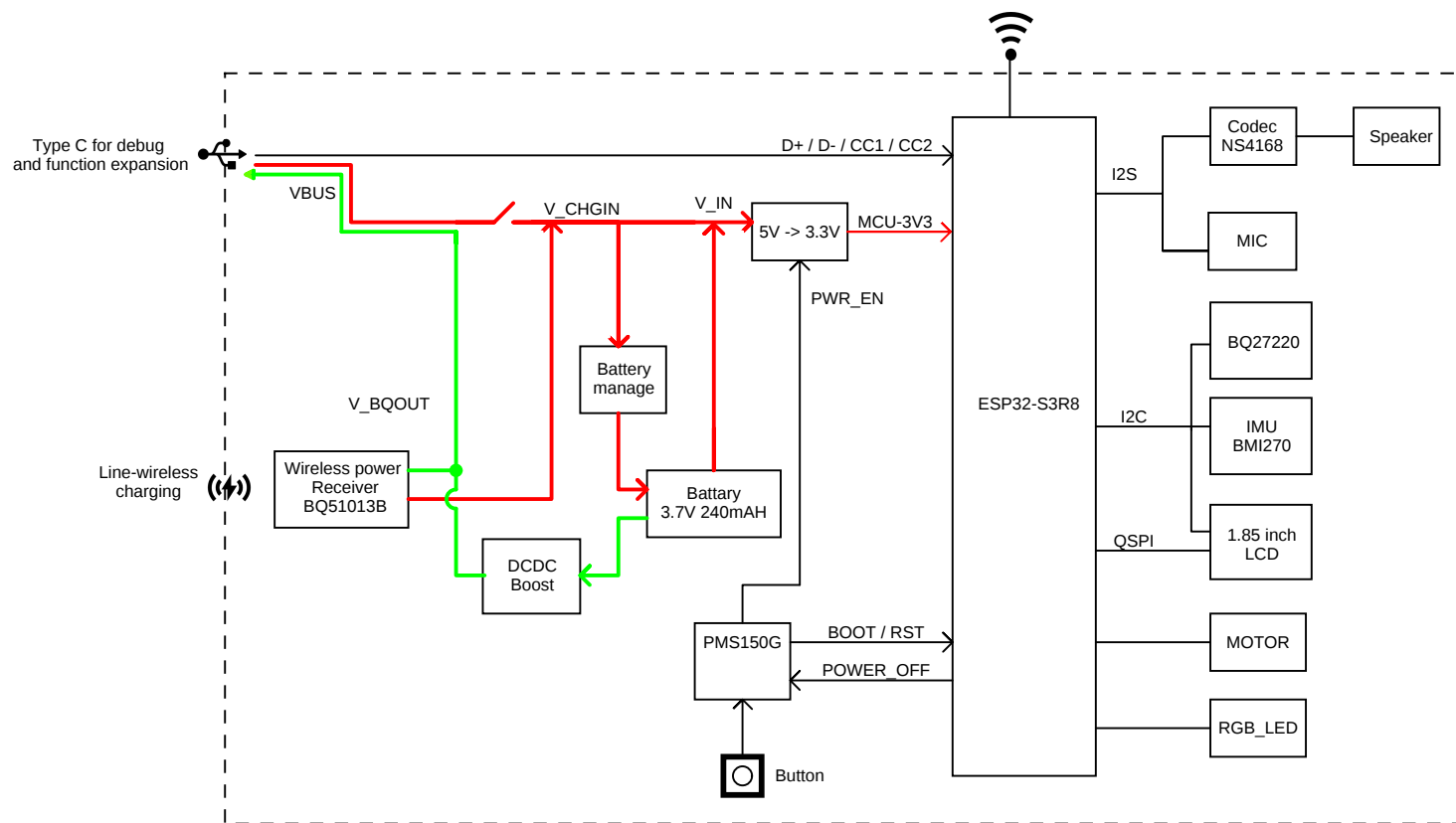
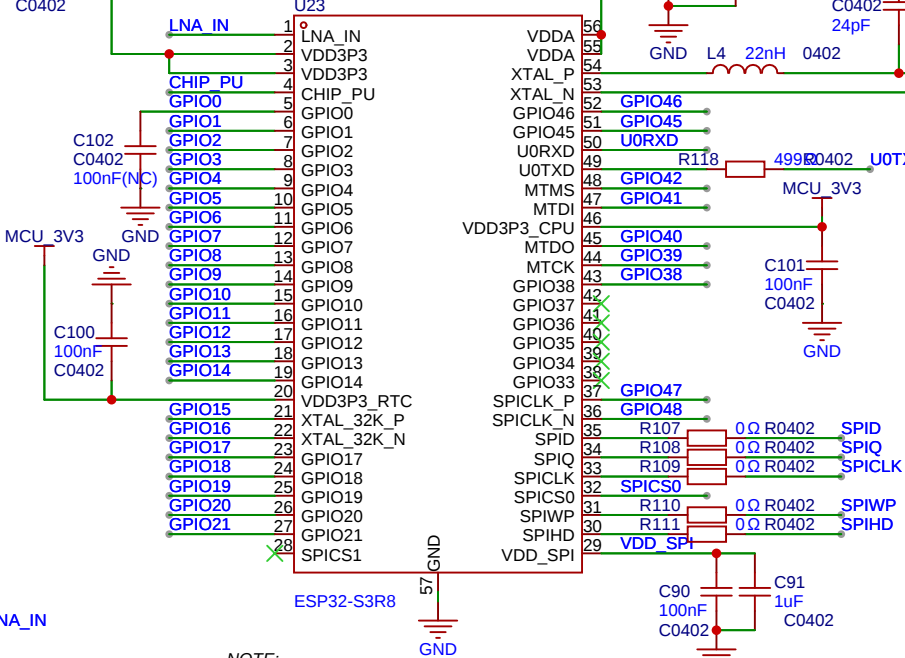
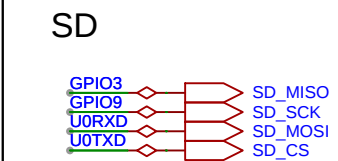
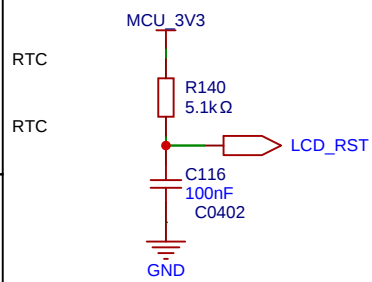
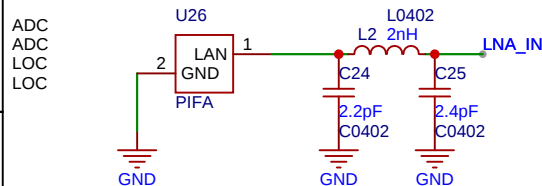
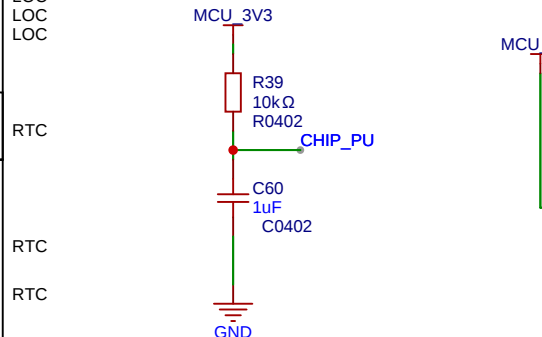
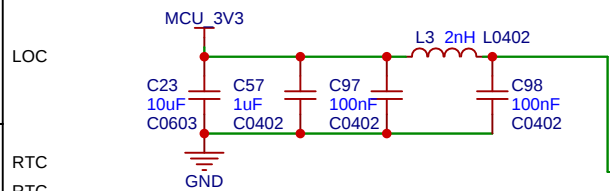
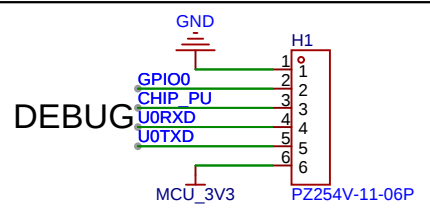
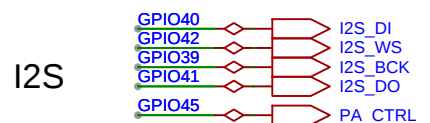
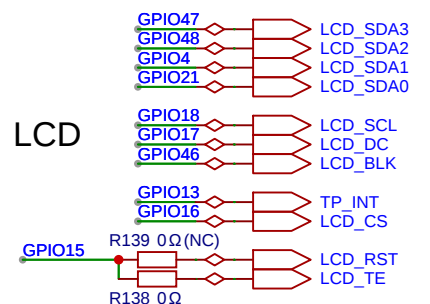
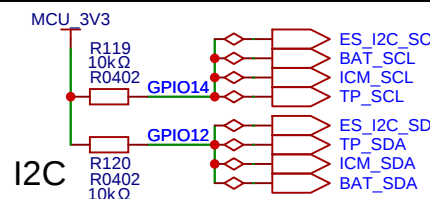
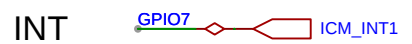
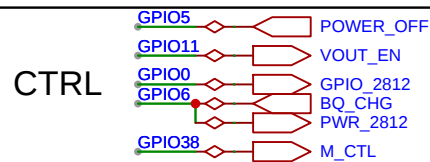
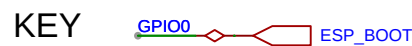
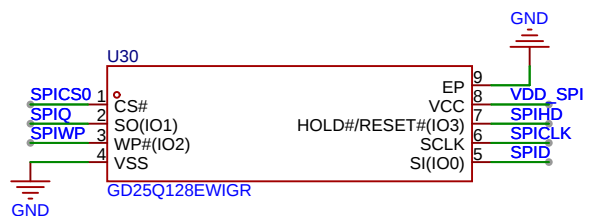




原理图	Schematic1_7			更新日期	2025-04-11
				创建日期	2025-02-14
图页	Overview			物料编码	
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审阅					
		版本	尺寸	页	1 共 5
		V1.0	A4		

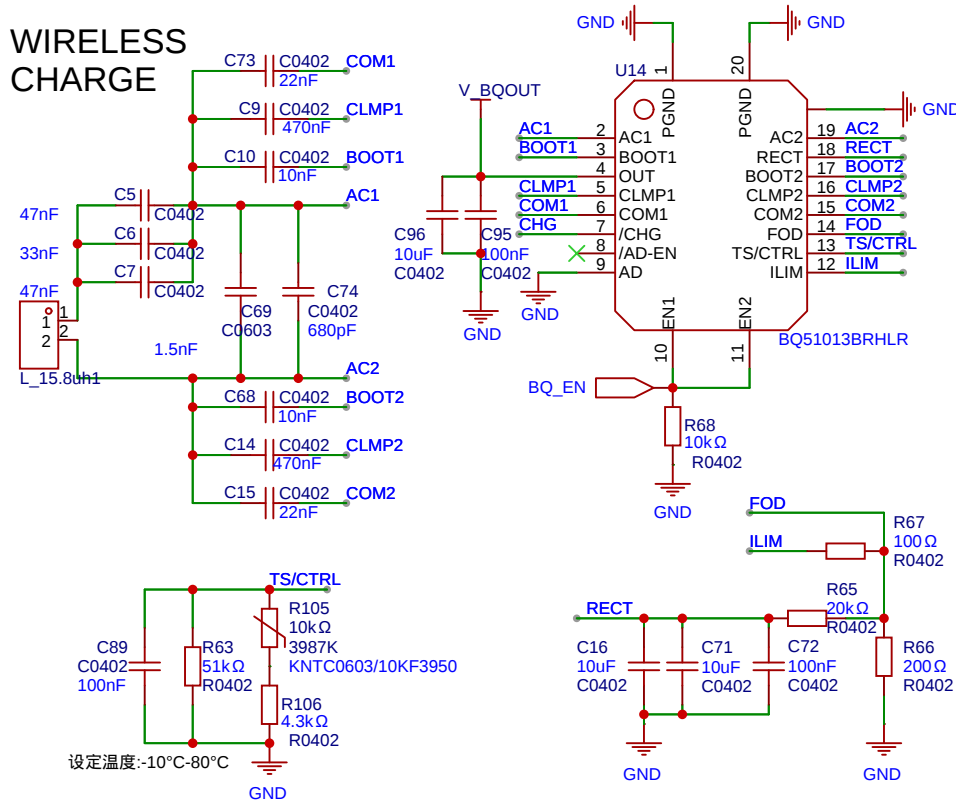


NOTE: *SPICS1 and IO33 ~ IO37 connect to the OSPI PSRAM and are not available for other use.
RTC GPIO 0-21*

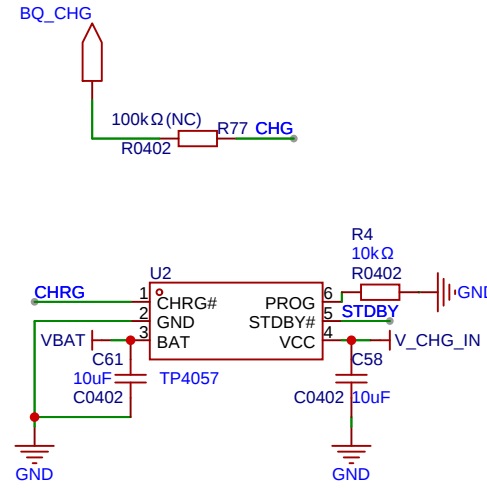


原理图	Schematic1_7			更新日期	2025-04-23
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审阅					
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		V1.0	A4		

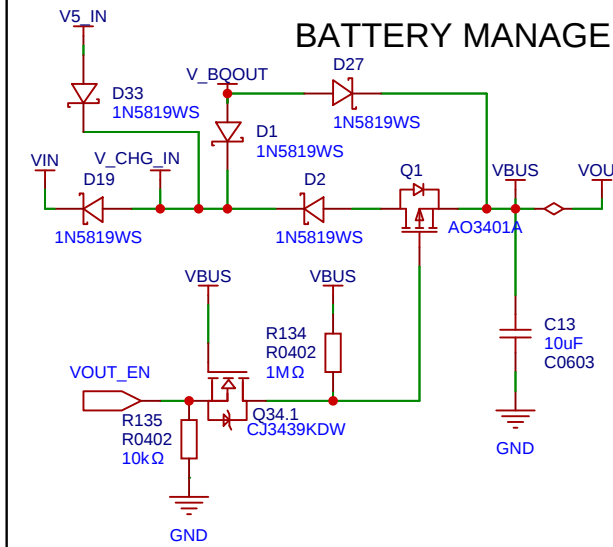
WIRELESS CHARGE



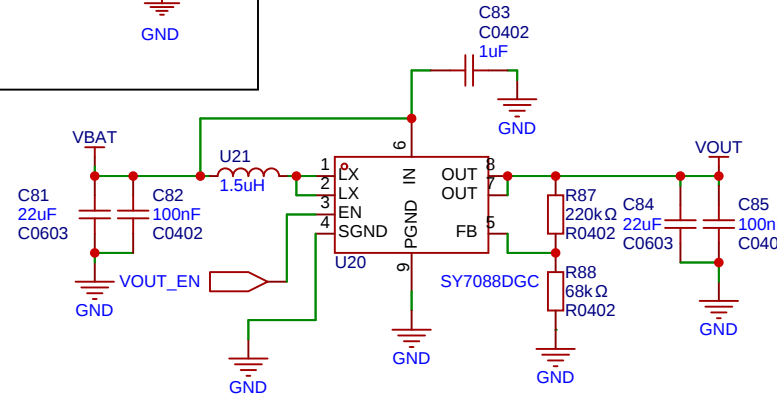
CHARGE



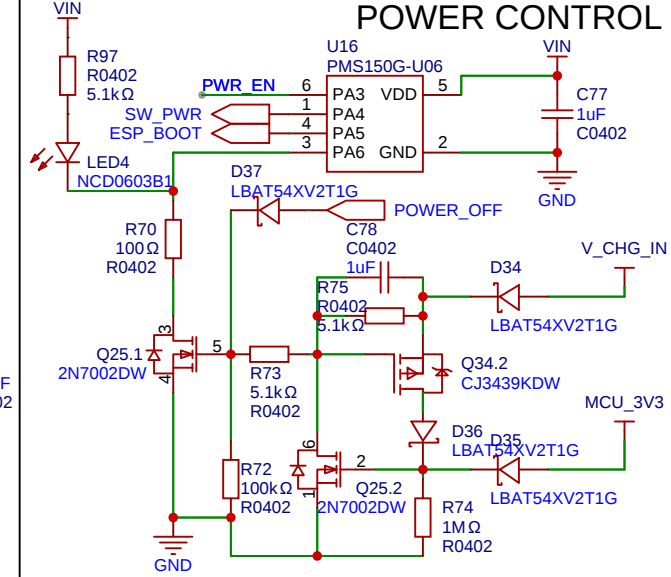
BATTERY MANAGE



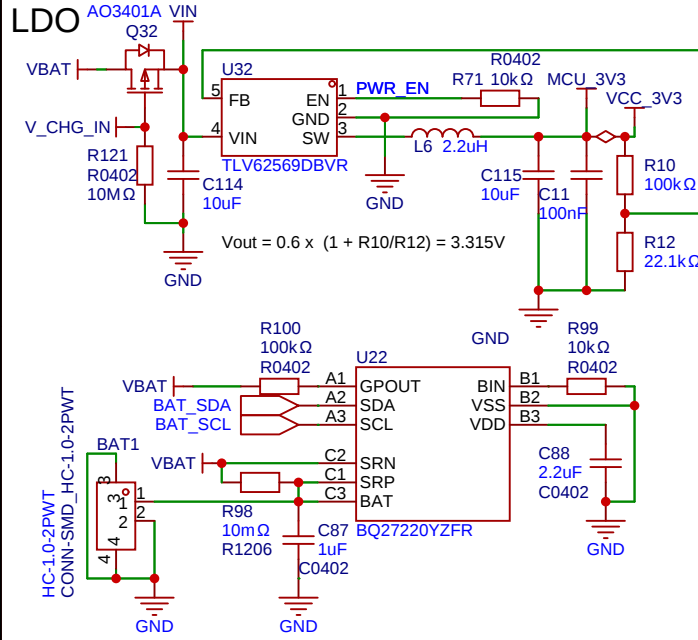
VBAT->5V



POWER CONTROL

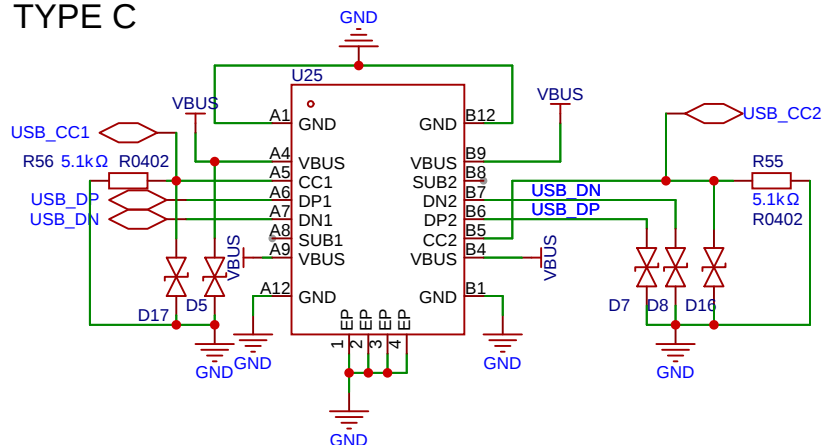


LDO

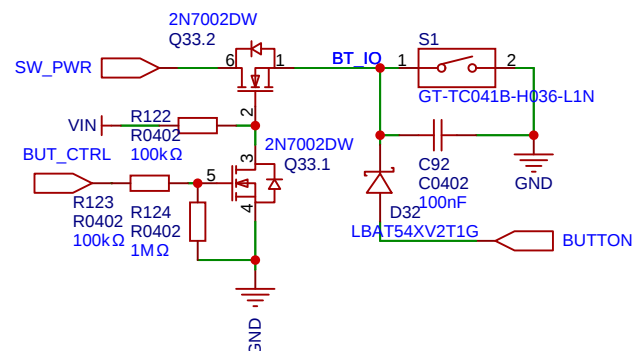


原理图	Schematic1_7			更新日期	2025-04-11
图页	Power			创建日期	2025-02-14
绘制				物料编码	
审阅					
				版本	尺寸
				V1.0	A4
				页	3 共 5

TYPE C

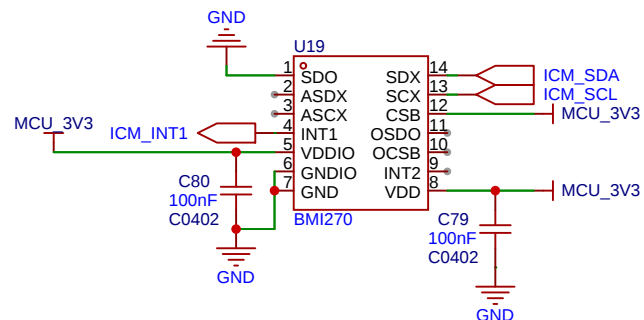


BUTTON

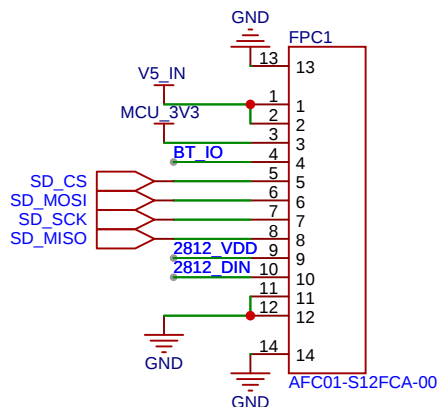


IMU

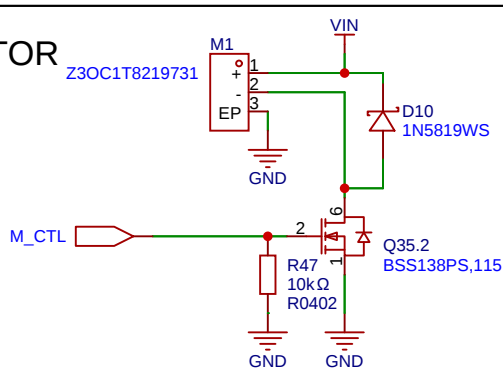
IIC ADDR : 0x68



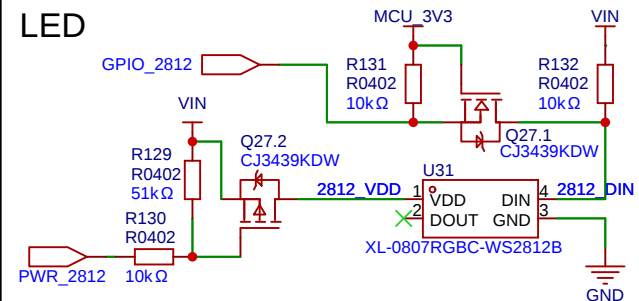
SUB BOARD



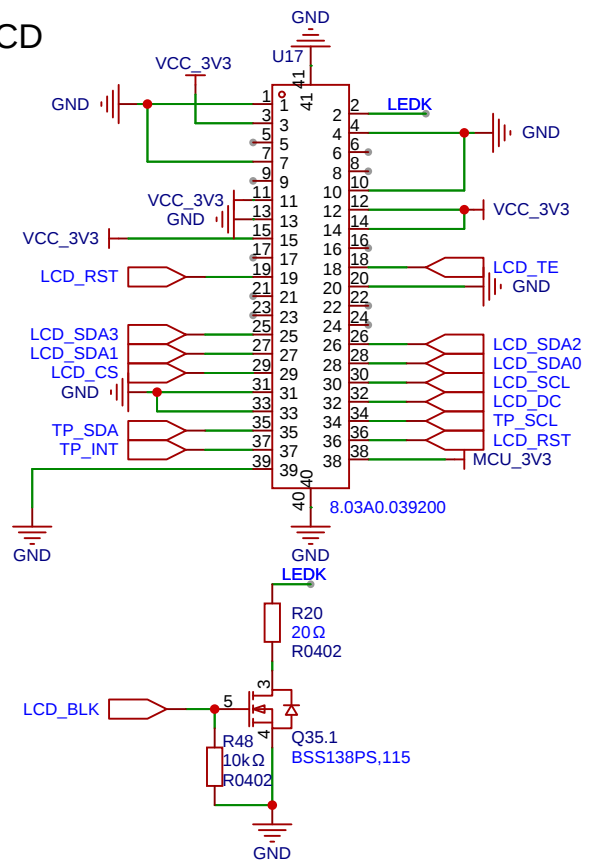
MOTOR



LED

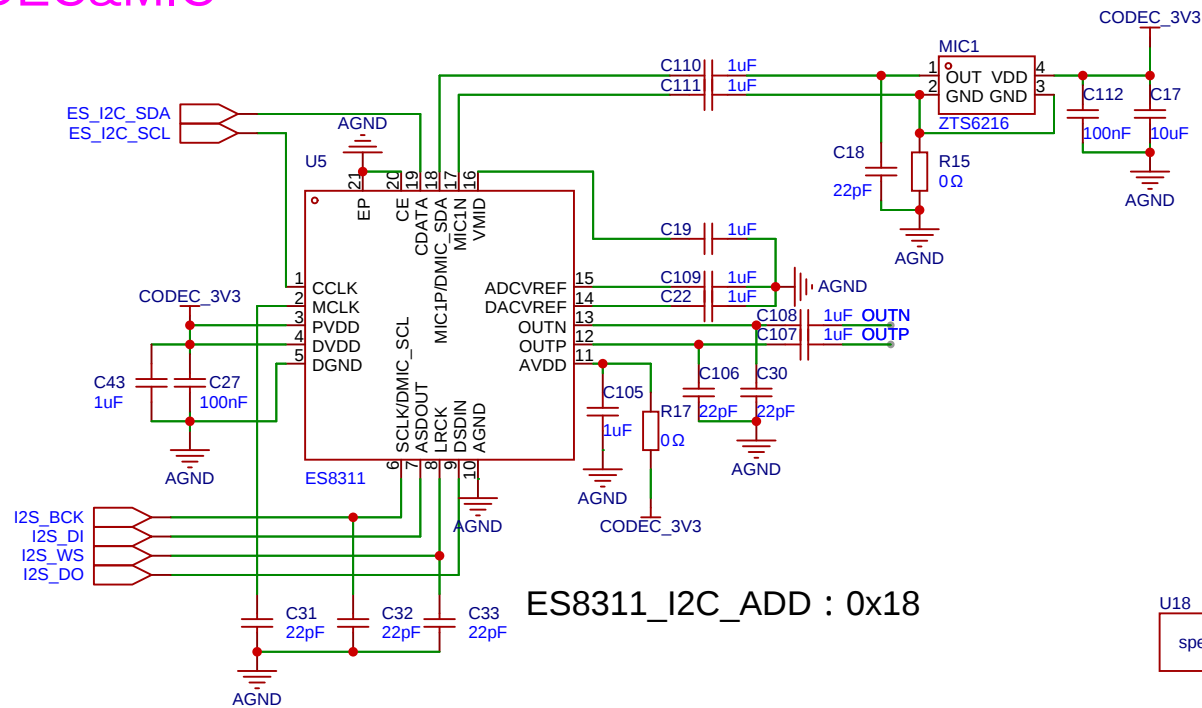


LCD

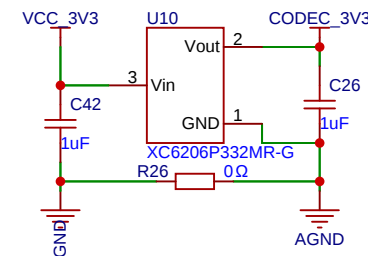


原理图	Schematic1_7			更新日期	2025-04-11
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图页	Interface			物料编码	
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审阅					
		版本	尺寸	页	4 共 5
		V1.0	A4		

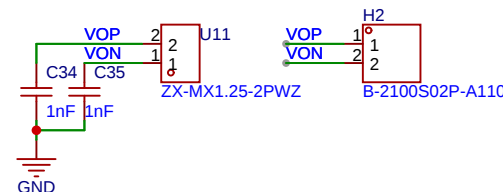
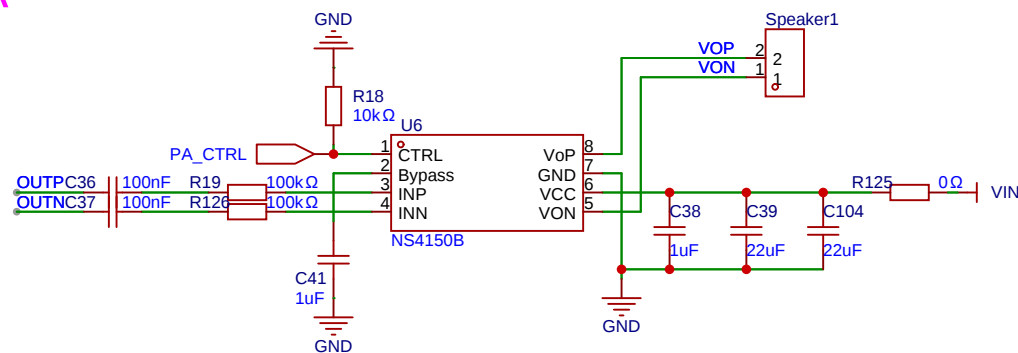
CODEC&MIC



CODEC_3V3



PA



原理图	Schematic1_7			更新日期	2025-04-11
				创建日期	2025-02-14
图页	CORDEC			物料编码	
绘制					
审阅					
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		V1.0	A4		