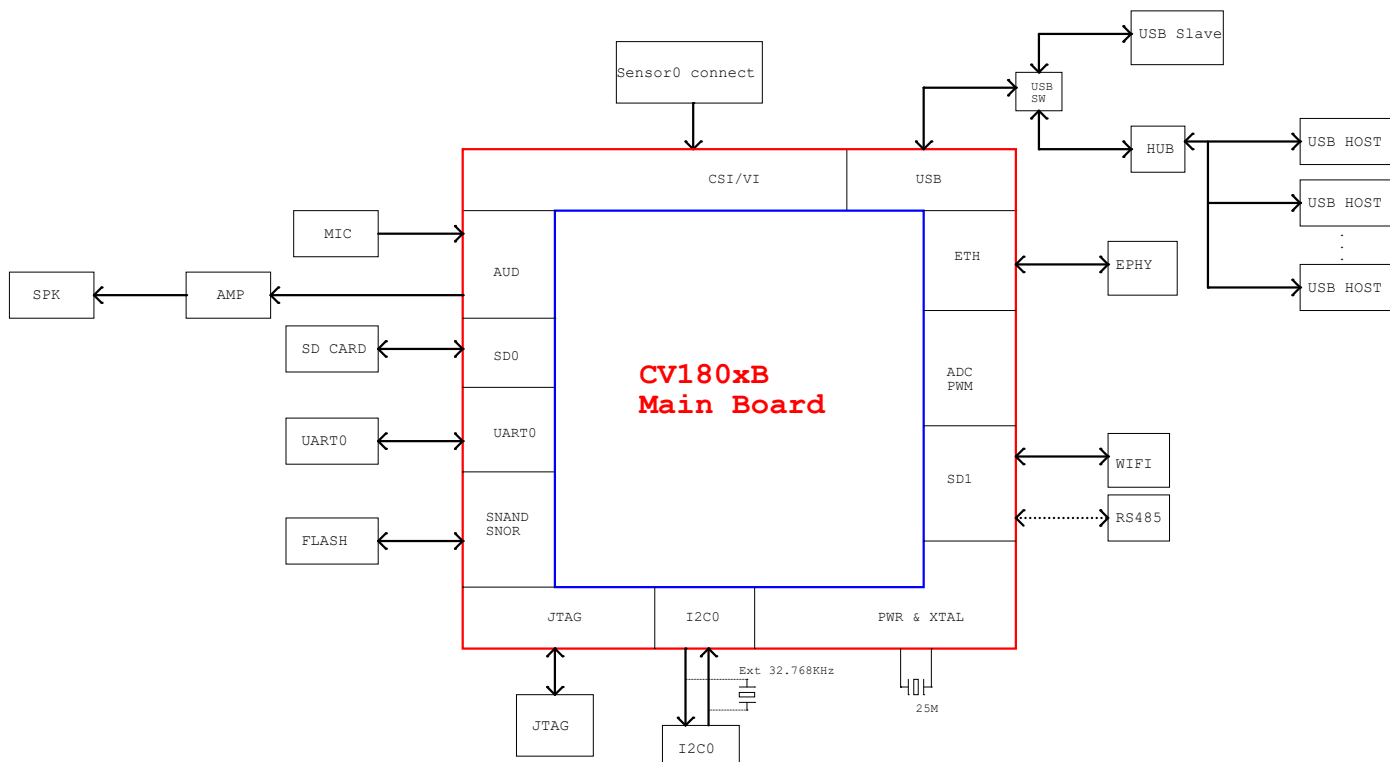
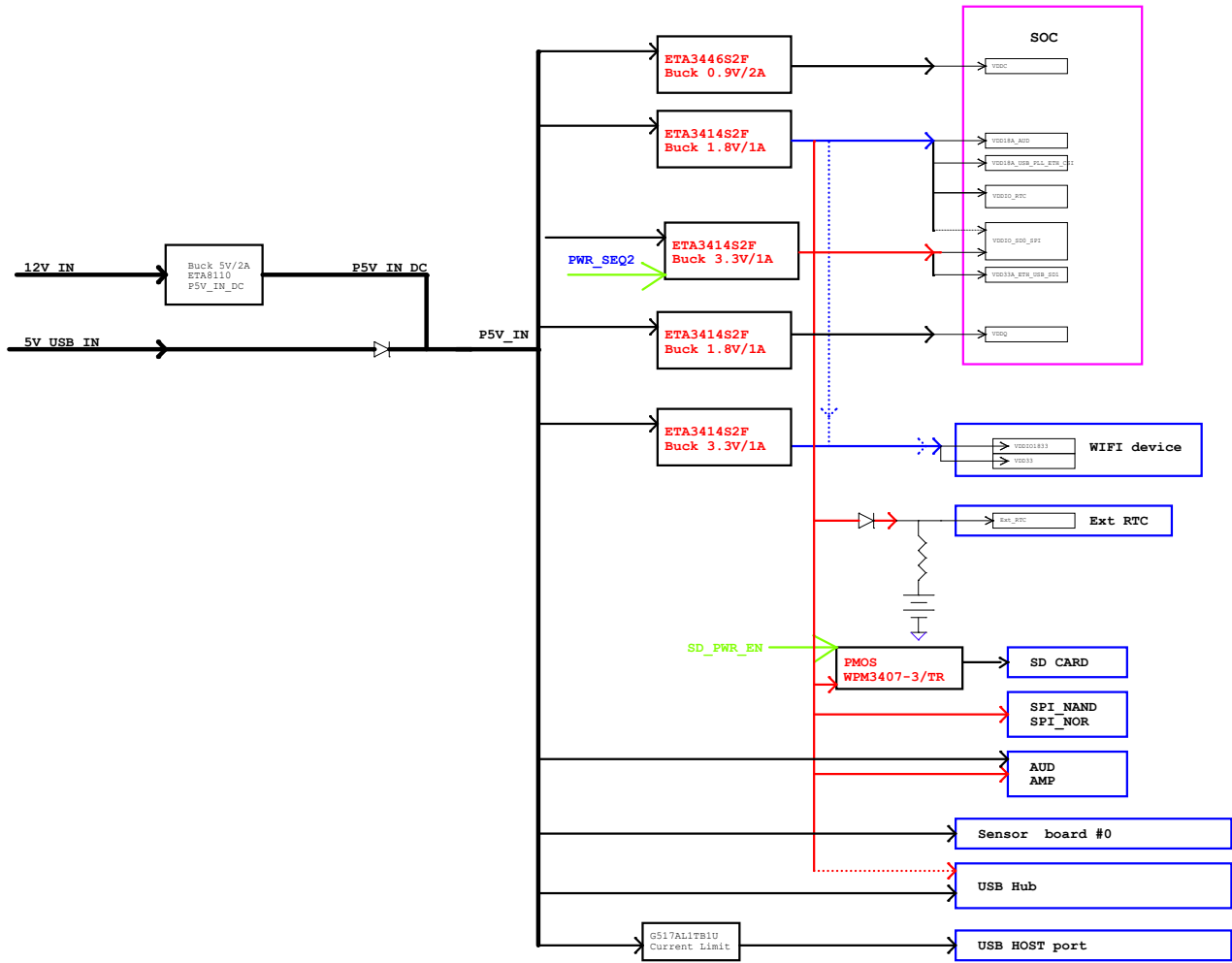
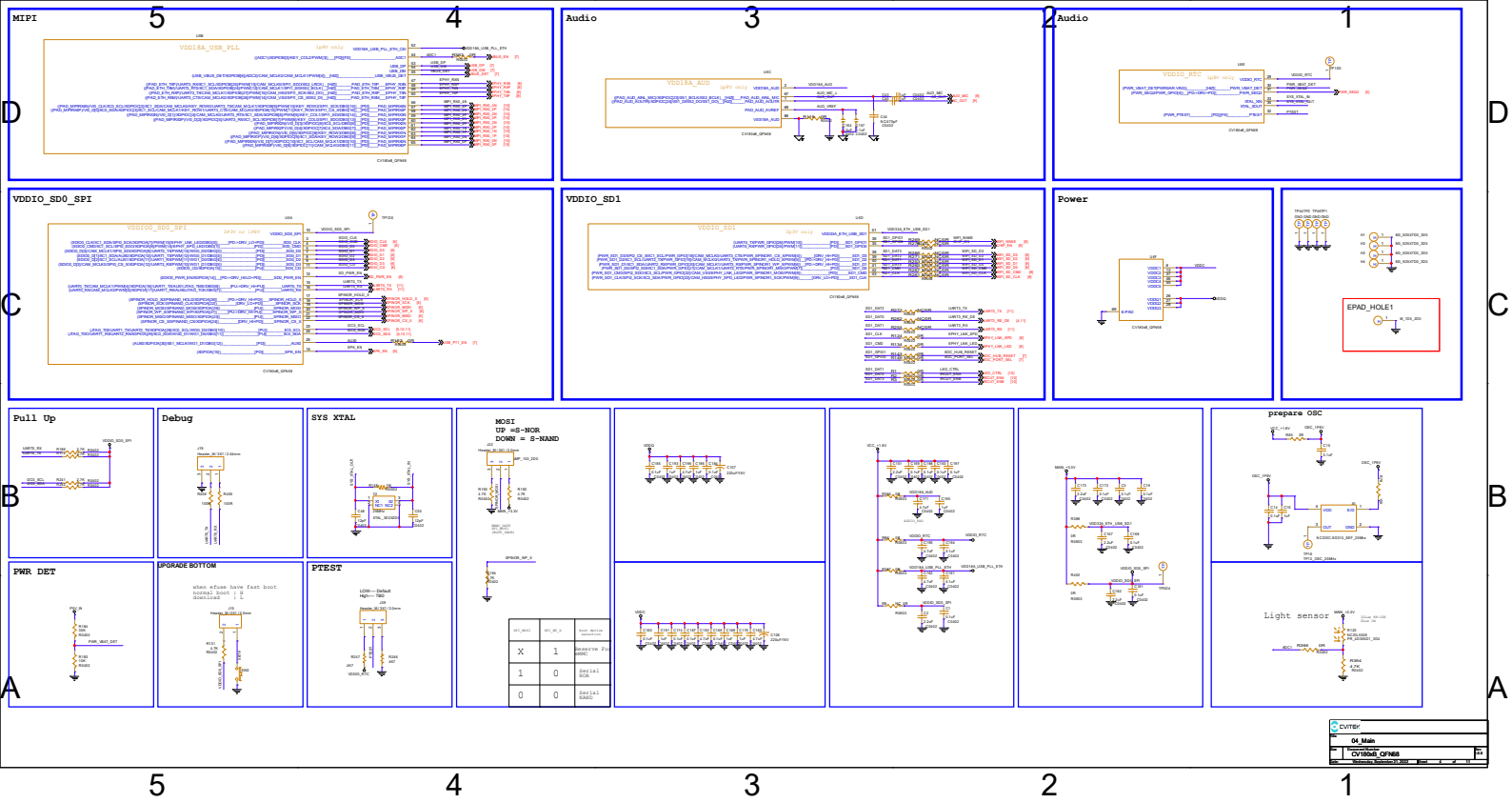
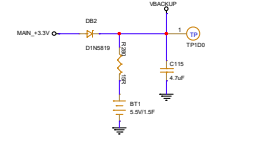
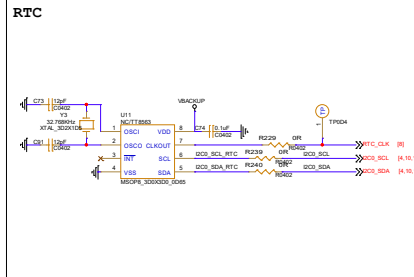
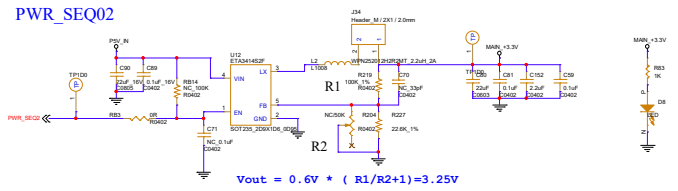
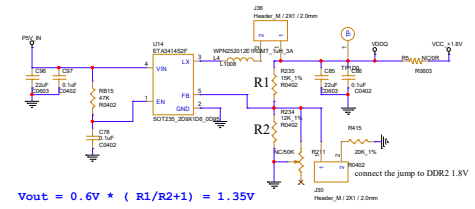



Block Diagram for CV180xB





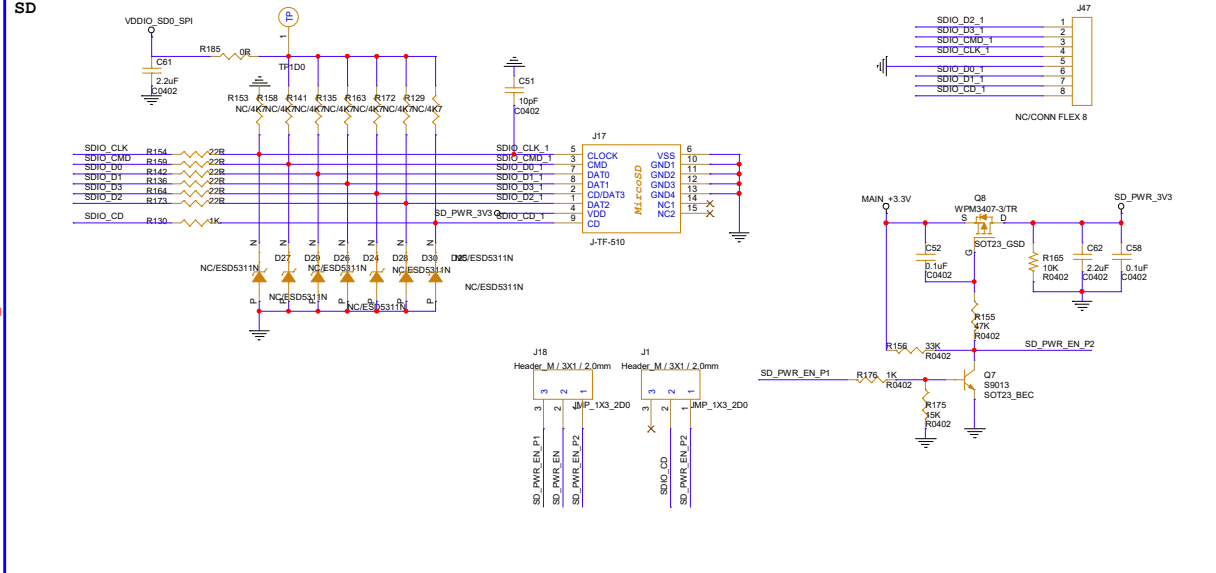


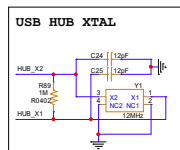
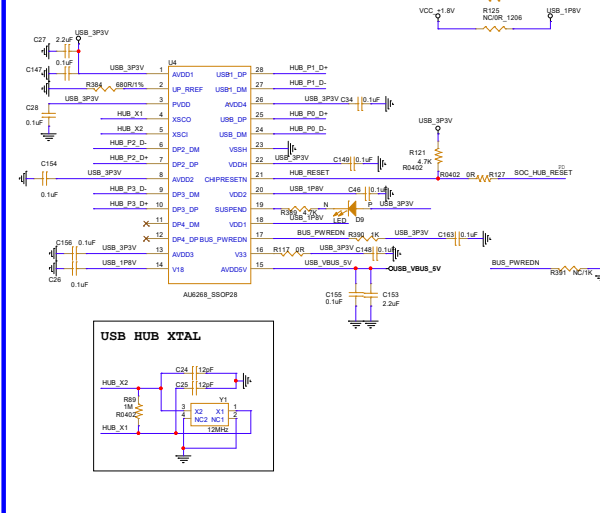
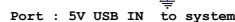
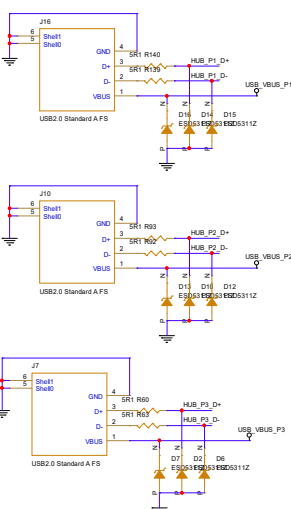
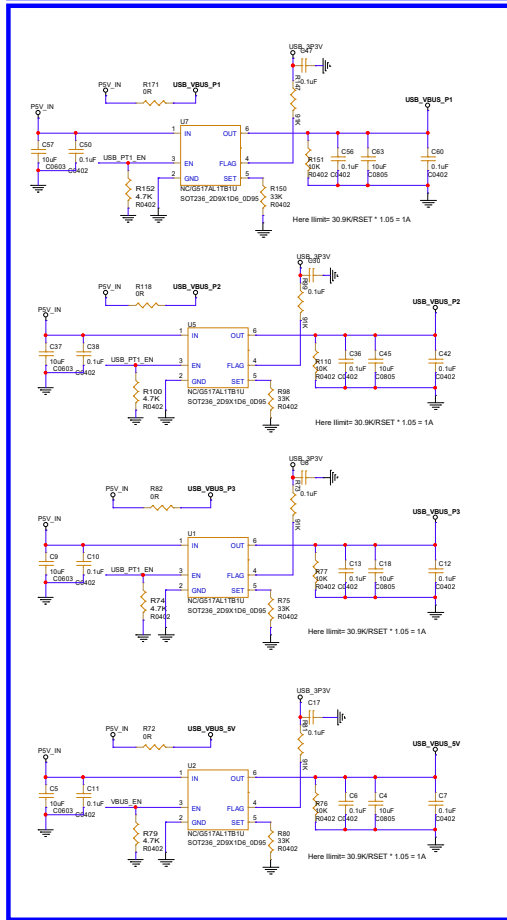


			
File			
05_System Power			
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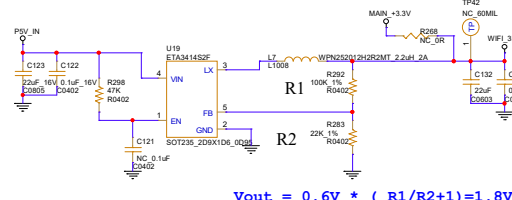
SDIO_CLK	>>>	SDIO_CLK	[4]
SDIO_CMD	>>>	SDIO_CMD	[4]
SDIO_D0	>>>	SDIO_D0	[4]
SDIO_D1	>>>	SDIO_D1	[4]
SDIO_D2	>>>	SDIO_D2	[4]
SDIO_D3	>>>	SDIO_D3	[4]
SDIO_CD	>>>	SDIO_CD	[4]
SD_PWR_EN	>>>	SD_PWR_EN	[4]

SPINOR_HOLD_X	>>>	SPINOR_HOLD_X	[4]
SPINOR_MISO	>>>	SPINOR_SCK	[4]
SPINOR_WP_X	>>>	SPINOR_MISO	[4]
SPINOR_MISO	>>>	SPINOR_WP_X	[4]
SPINOR_CS_X	>>>	SPINOR_MISO	[4]
		SPINOR_CS_X	[4]

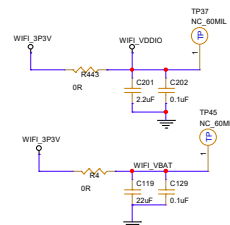
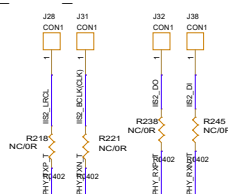
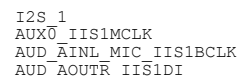
[illegible][illegible]

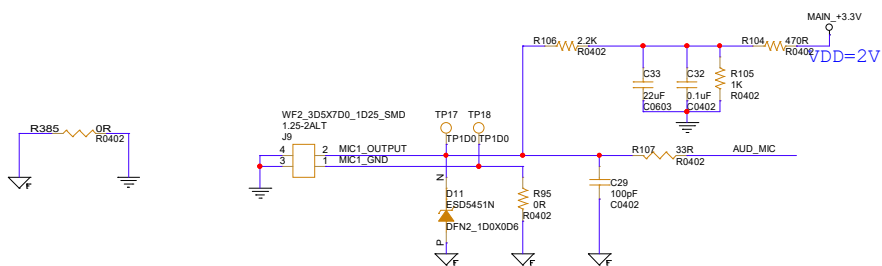
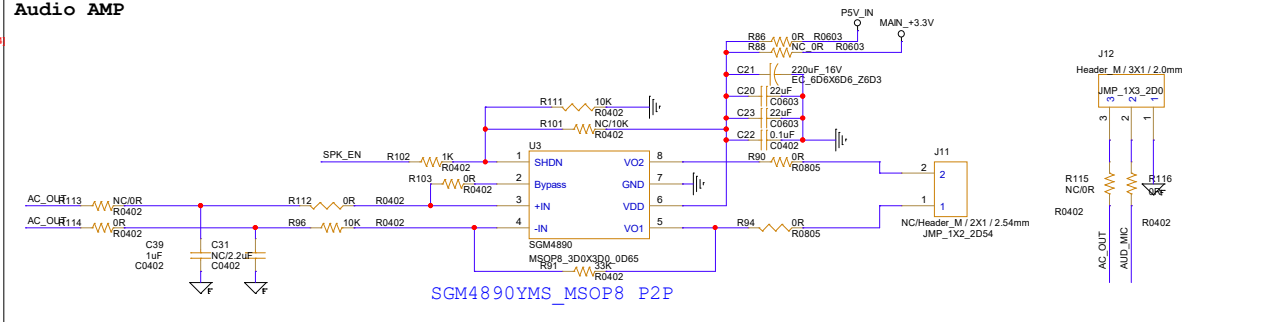



WIFI



$$V_{out} = 0.6V * (R1/R2+1) = 1.8V$$

[illegible]



			
File			
09_Audio			
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NET

I2C0_SCL >> I2C0_SCL [4,5,11]
I2C0_SDA >> I2C0_SDA [4,5,11]

MIPI_RX0_0N >> MIPI_RX0_0N [4]
MIPI_RX0_0P >> MIPI_RX0_0P [4]
MIPI_RX0_1N >> MIPI_RX0_1N [4]
MIPI_RX0_1P >> MIPI_RX0_1P [4]
MIPI_RX0_2N >> MIPI_RX0_2N [4]
MIPI_RX0_2P >> MIPI_RX0_2P [4]
MIPI_RX0_3N >> MIPI_RX0_3N [4]
MIPI_RX0_3P >> MIPI_RX0_3P [4]
MIPI_RX0_4N >> MIPI_RX0_4N [4]
MIPI_RX0_4P >> MIPI_RX0_4P [4]

LED_CTRL >> LED_CTRL [4]
IRCUT_ENA >> IRCUT_ENA [4]
IRCUT_ENB >> IRCUT_ENB [4]

J5

Header_M / 3X1 / 2.0mm

SE CLK1
CAM_MCLK0
JMP_1X3_20
SE CLK2

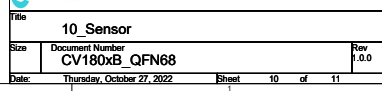
I2C0_SCL R435 0R I2C0_SCL_1
I2C0_SDA R441 0R I2C0_SDA_1

MIPI_RX0_1N R33 0R CAM_RST0
MIPI_RX0_1P R34 0R CAM_I2C2_SDA
MIPI_RX0_0N R9 0R CAM_I2C2_SCL
MIPI_RX0_0P R10 0R CAM_MCLK0

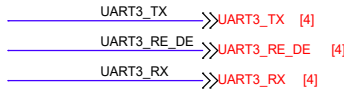
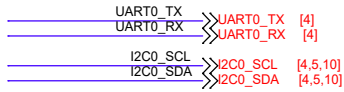
R402
R402
R402
R402
R402

VCC +1.8V

CAM_I2C2_SCL R8 2.7K R402
CAM_I2C2_SDA R7 2.7K R402

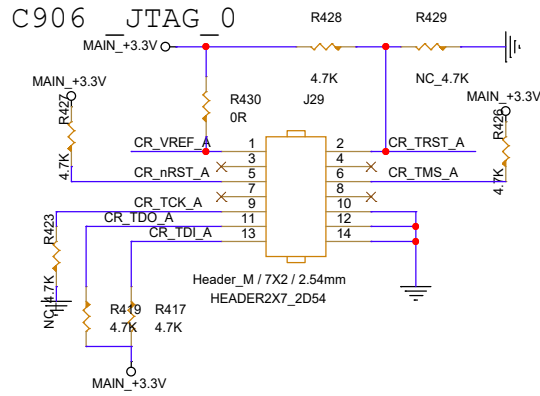


NET

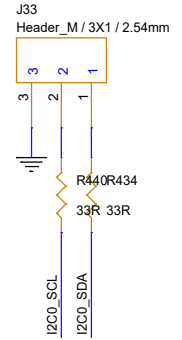


JTAG

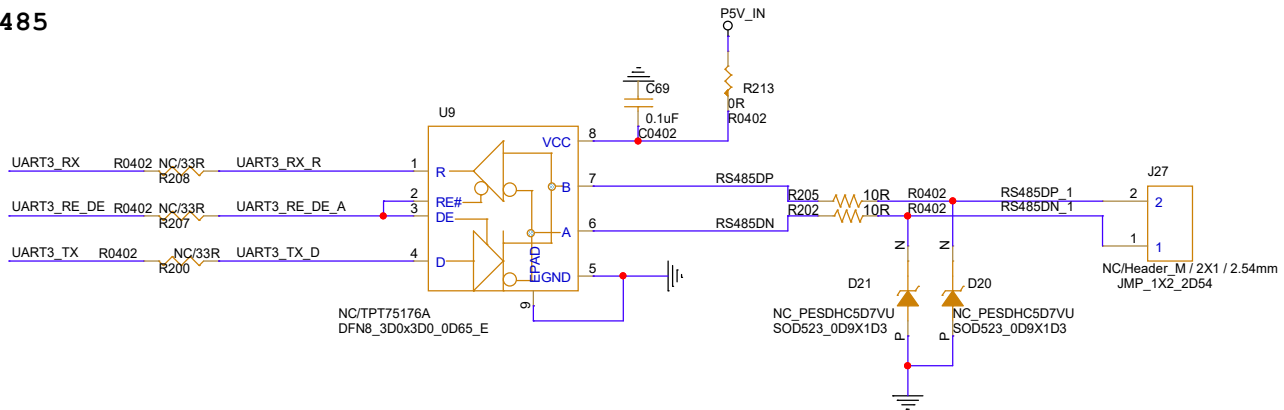
I2C0_SCL	R418	0R	R0402	CR_TDI_A
I2C0_SDA	R420	0R	R0402	CR_TDO_A
UART0_RX	R422	0R	R0402	CR_TCK_A
UART0_TX	R425	0R	R0402	CR_TMS_A



DEBUG IIC0



RS485



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